



ELECTRONICS

VD사업부

Ver.\_\_\_\_\_

# 승 인 서

SEC CODE	
품명 (Description)	LCD-Panel
규격 (Specification)	55inch, FHD, 120Hz
Maker P/N	LSC550HJ02
적용기종 (적용모델)	
특기사항	

기안 (DRAWING)	심사 (CHECK)	합의 (AGREEMENT)	승인 (APPROVAL)
2011. .	2011. .	2011. .	2011. .
승인기간	승인일로부터		

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Approval Spec.

# SAMSUNG DISPLAY

Customer :Visual Display Division

DATE : 4. Jan. 2012

**SAMSUNG TFT-LCD**

**MODEL:LSC550HJ02**

Any Modification of Specification is not allowed without SDC's Permission.

**NOTE :**

## *Customer's Approval*

SIGNATURE

DATE

APPROVED BY

*Heo Jeongmin*

DATE

4. Jan. 2011

PREPARED BY

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DATE

4. Jan. 2011

**Samsung Display Co . , LTD.**

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## Revision History

Date	Rev. No	Page	Summary
9.Nov.2011	000	all	<b>First issued</b>
22.Dec.2011	001	5	<b>Display Colors 10bit → 8bit</b>
22.Dec.2011	002	10	<b>TFT LCD Module</b>
22.Dec.2011	003	16	<b>Interface Parameter</b>
22.Dec.2011	004	16	<b>Frame Frequency</b>
27.Dec.2011	005	7	<b>Transmittance</b>
28.Dec.2011	006	7	<b>Color Chromaticity (CIE 1931) Ry 0.340 → 0.330</b>
4.Jan.2012	007	27	<b>Storage Condition</b>

# General Description

## Description

LSC550HJ02 is a color active matrix liquid crystal display(LCD) that uses amorphous silicon TFT( Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit. The resolution of a 55" is 1920 x 1080 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

## Features

- High contrast & aperture ratio with wide color gamut
- SVA (Super Vertical Align) mode
- Wide viewing angle ( $\pm 178^\circ$ )
- High speed response
- FHD resolution (16:9)
- 4Ch LVDS (Low Voltage Differential Signaling) interface

# General Information

Items	Specification	Unit	Note
Panel Size	1227.6(H) x 700.5(V)	mm	$\pm 1.0\text{mm}$
	1.76(D)		Max
Weight	3.15(Max)	kg	
Pixel Pitch	0.210(H) X 0.630(V)	mm	
Active Display Area	1209.6(H) X 680.4(V)	mm	
Surface Treatment	Haze 2%, Hard-coating (2H)		
Display Colors	16.7M (8Bits Dithering)	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		

## 1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

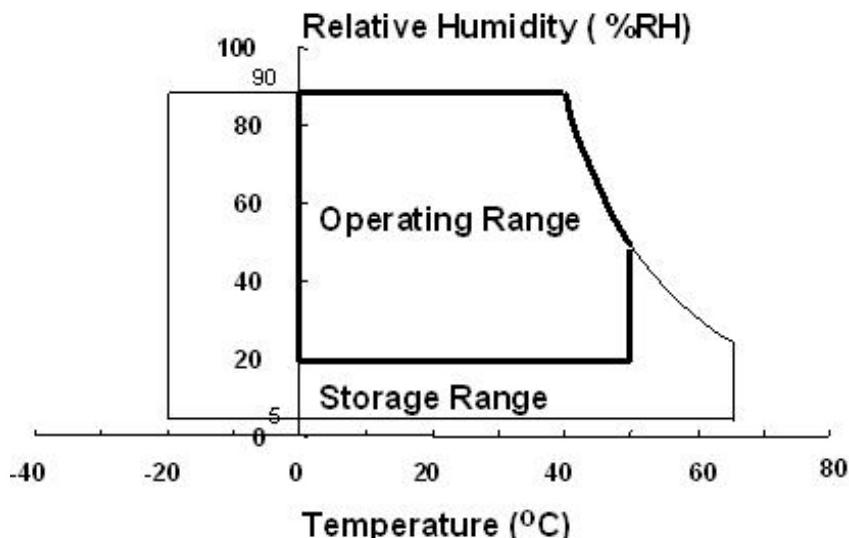
(V<sub>SS</sub> = 0 V)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V <sub>DD</sub>	10.8	13.2	V	(1)
Storage temperature	T <sub>STG</sub>	-20	65	°C	(2)
Glass surface temperature (Operation)	Center	T <sub>OPR</sub>	0	50	°C

Note (1) Ta= 25 ± 2 °C

(2) Temperature and relative humidity range are shown in the figure below.

- a. 90 % RH Max. (Ta ≤ 39 °C)
- b. Relative Humidity is 90% or less. (Ta > 39 °C)
- c. No condensation



## 2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : D65, RD-80S, EZ-Contrast

(Ta = 25 ± 2°C, VDD=12V)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio (Center of screen)		C/R	Normal $\theta L, R = 0$ $\theta U, D = 0$ Viewing Angle	3,000	5,000	-		(1) D65	
Response Time	G-to-G	Tg		-	8	15	msec	(3) RD-80S	
Transmittance		T		6.045	6.5	-	%	(4) D65	
Color Chromaticity (CIE 1931)	Red	Rx		0.640	TYP. -0.03	TYP. +0.03	(5),(6) D65		
		Ry		0.330					
	Green	Gx		0.300					
		Gy		0.600					
	Blue	Bx		0.150					
		By		0.060					
	White	Wx		0.280					
		Wy		0.290					
sRGB Concordance		-		-	98.5	-	%	(5) D65	
Color Temperature		-		-	10,000	-	K		
Viewing Angle	Hor.	$\theta_L$	C/R ≥ 10	75	89	-	Degree	(6) EZ-Contrast	
		$\theta_R$		75	89	-			
	Ver.	$\theta_U$		75	89	-			
		$\theta_D$		75	89	-			
Transmittance Uniformity (9 Points)		T <sub>uni</sub>		-	-	10	%	(2) D65	

### Notice

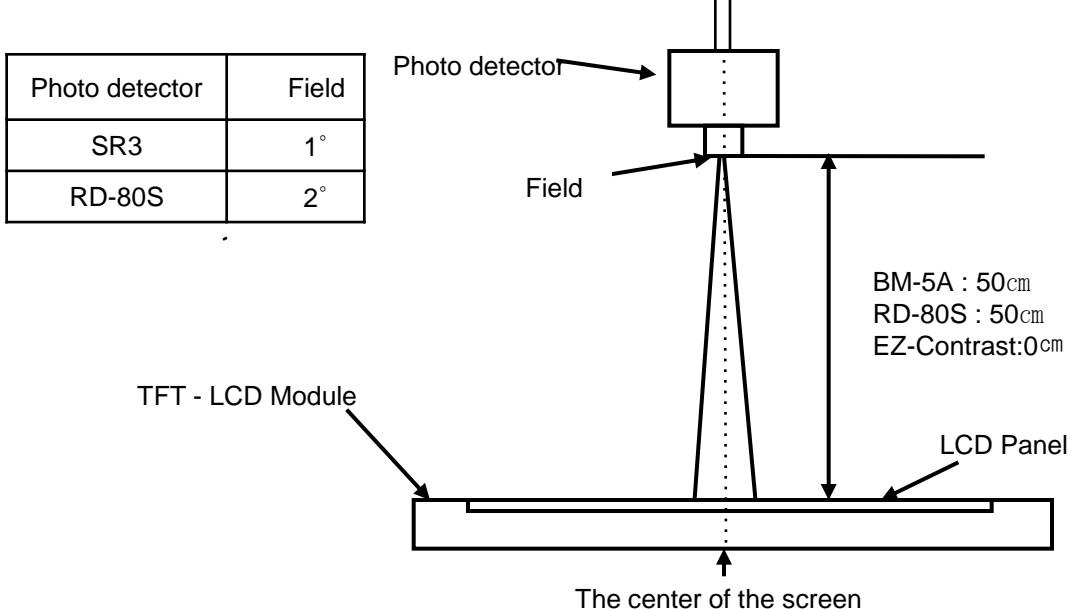
#### (a) Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after operating the Panel at the given temperature for stabilization of the standard light(SEC use standard illuminant D65 Media).

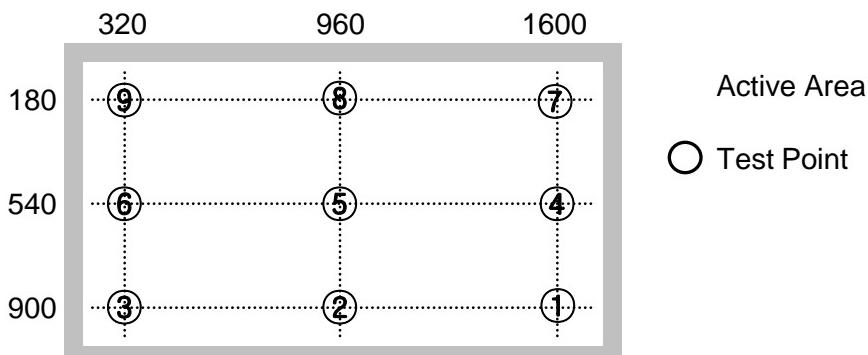
This should be measured in the center of screen. Environment condition : Ta = 25 ± 2 °C

#### (b) D65 Media means general available light source.

Color Temperature is 6504K , a color coordinate is Wx 0.2948, Wy 0.322,  
Luminance is 3389cd/m<sup>2</sup>



#### - Definition of test point



#### Note (1) Definition of Contrast Ratio (C/R)

- : Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel
- : The measurement goes in D65 Standard Light Source

$$C / R = \frac{G_{\max}}{G_{\min}}$$

Gmax : Luminance with all pixels white  
 Gmin : Luminance with all pixels black

Note (2) Definition of 9 points Transmittance uniformity (Test pattern : Full White)

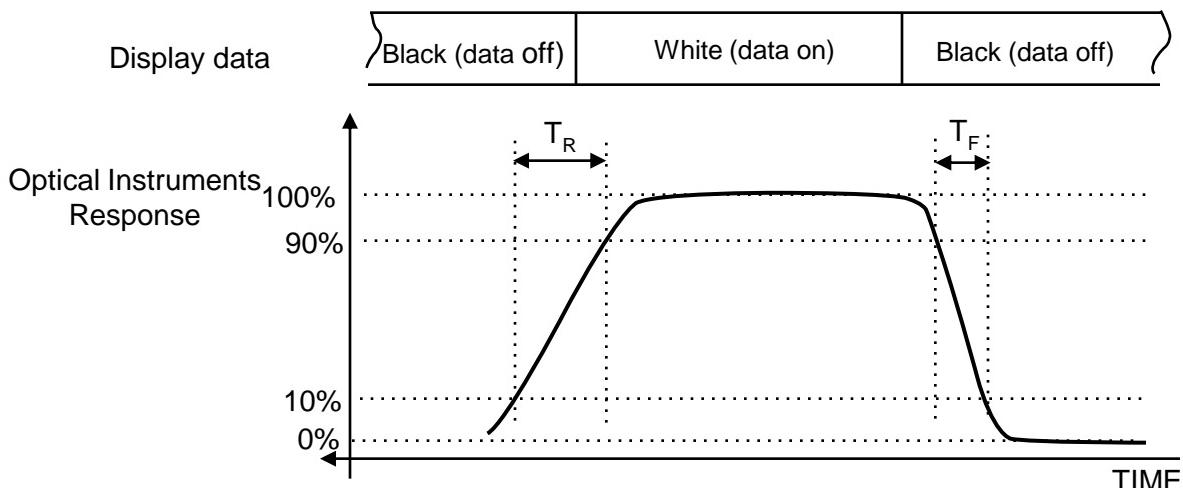
The measurement goes in D65 Standard Light Source

$$T_{uni} = \frac{(T_{max} - T_{min})}{T_{max}} \times 100$$

T<sub>max</sub> : Maximum Transmittance

T<sub>min</sub> : Minimum Transmittance

Note (3) Definition of Response time : Sum of Tr, Tf



※ G-to-G : Average response time between Gray to Gray (Scale)

Note (4) Definition of Transmittance : Transmittance at center point ⑤

The measurement goes in D65 Standard Light Source

Note (5) Definition of Color Chromaticity (CIE 1931)

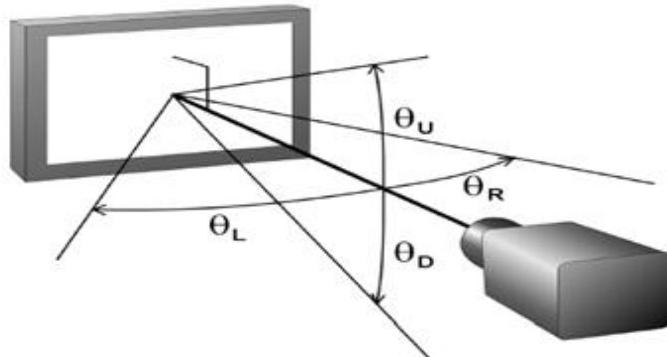
Color coordinate of Red, Green, Blue & White at center point ⑤

The measurement goes in D65 Standard Light Source

Note (6) Definition of Viewing Angle

: Viewing angle range ( $C/R \geq 10$ )

The measurement goes in D65 Standard Light Source



### 3. Electrical Characteristics

#### 3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$

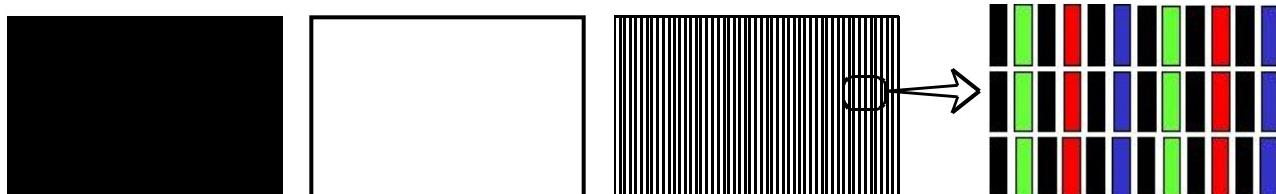
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply	VDD	10.8	12.0	13.2	V	(1)
Current of Power Supply	IDD	-	1100	1200	mA	(2),(3)
		-	1100	1200	mA	
		-	1700	1800	mA	
Vsync Frequency	fV	-	60	-	Hz	
Hsync Frequency	fH	-	67.5	-	kHz	
Main Frequency	Fdclk	-	148.5	-	MHz	
Rush Current	IRUSH	-	4	6	A	(4)

Note (1) The ripple voltage should be controlled under 10% of  $V_{DD}$ .

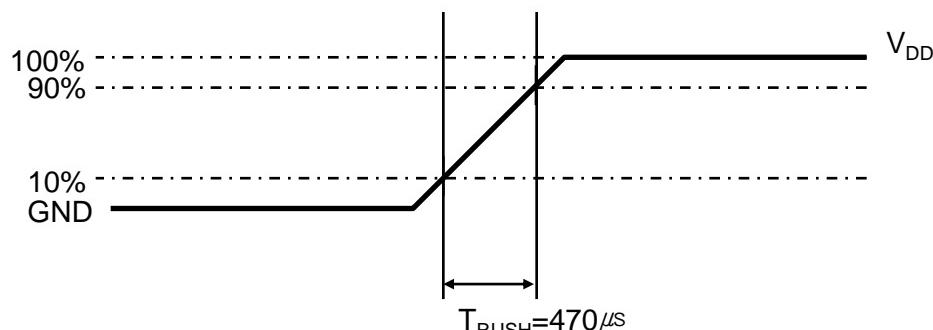
(2)  $f_V = 120$  Hz,  $f_{DCLK} = 297$  MHz,  $V_{DD} = 12.0$  V, DC Current.

(3) Power dissipation check pattern (LCD Module only)

a) Black Pattern      b) White Pattern      c) N-pattern

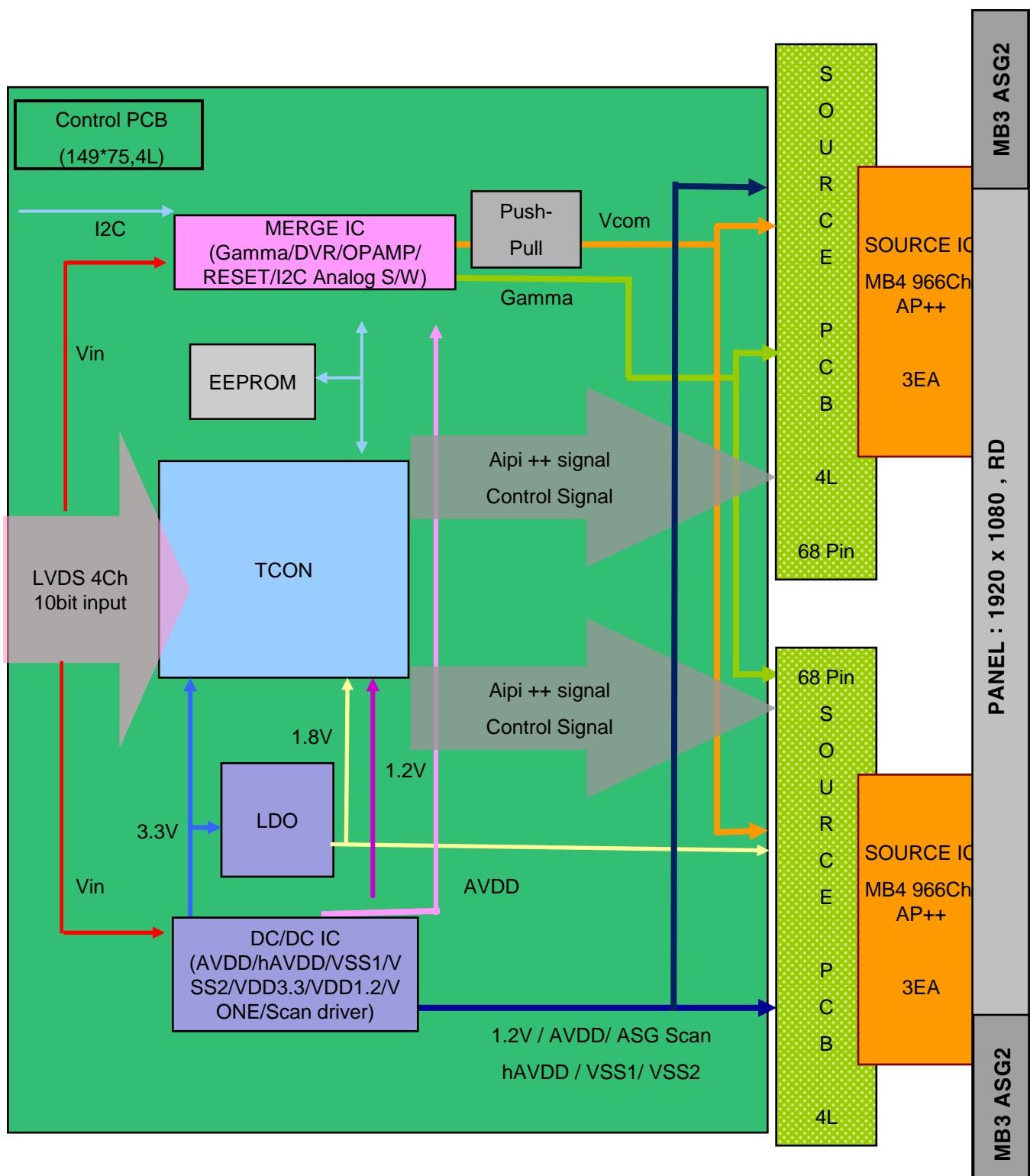


(4) Measurement Conditions



Rush Current  $I_{RUSH}$  can be measured when  $T_{RUSH}$  is  $470\mu\text{s}$ .

## 4. Block Diagram



## 5. Input Terminal Pin Assignment

### 5.1. Input Signal & Power

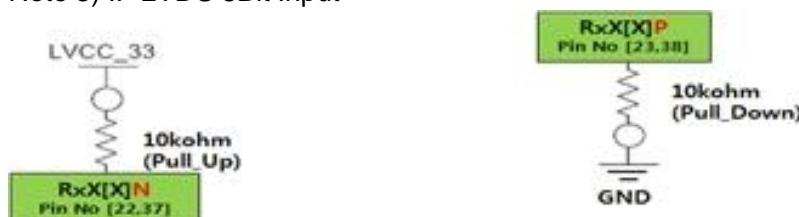
Pin	Description		Pin	Description	
1	Even LVDS Signal	Vdd (12V)	26	Rx4[0]P	
2		Vdd (12V)	27	Rx4[1]N	
3		Vdd (12V)	28	Rx4[1]P	
4		Vdd (12V)	29	Rx4[2]N	
5		Vdd (12V)	30	Rx4[2]P	
6		N.C	31	GND	
7		GND	32	Rx4CLK-	
8		GND	33	Rx4CLK+	
9		GND	34	GND	
10		Rx2[0]N	35	Rx4[3]N	
11		Rx2[0]P	36	Rx4[3]P	
12		Rx2[1]N	37	Rx4[4]N	NOTE3
13		Rx2[1]P	38	Rx4[4]P	NOTE3
14		Rx2[2]N	39	GND	
15		Rx2[2]P	40	No connection	NOTE1
16		GND	41	No connection	
17		Rx2CLK-	42	No connection	
18		Rx2CLK+	43	No connection	
19		GND	44	No connection	
20		Rx2[3]N	45	LVDS_SEL	NOTE2
21		Rx2[3]P	46	No connection	
22		Rx2[4]N	47	No connection	NOTE1
23		Rx2[4]P	48	No connection	
24	GND		49	No connection	NOTE1
25	Even LVDS	Rx4[0]N	50	No connection	
			51	No connection	

Note 1) NOT CONNECTED : THIS PINS ARE ONLY USED FOR SEC INTERNAL OPERATIONS.

Note 2) LVDS OPTION : IF THIS PIN : HIGH (3.3 V) → NORMAL NS LVDS FORMAT

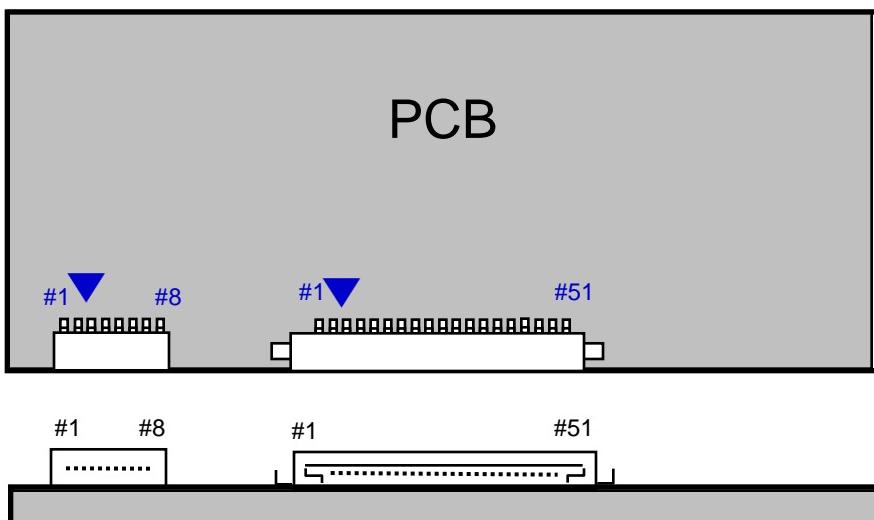
OTHERWISE : LOW (GND) OR OPEN(NC) → JEIDA LVDS FORMAT

Note 3) IF LVDS 8Bit Input



Aging CNT Pin Map								
NO	1	2	3	4	5	6	7	8
NAME	12V	12V	Gnd	A_EN (ActiveLow_ InputCNT)	WP	SDA	SCL	Gnd

Note(1) Pin number starts from Bottom side



**Fig. Connector diagram**

- a. Power GND pins should be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pin should be separated from other signal or power.

## 5.2 LVDS Interface

- LVDS Receiver : Tcon (merged) - Data Format (JEIDA)

	LVDS pin	JEIDA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2
	TxIN/RxOUT1	R3
	TxIN/RxOUT2	R4
	TxIN/RxOUT3	R5
	TxIN/RxOUT4	R6
	TxIN/RxOUT6	R7
	TxIN/RxOUT7	G2
	TxIN/RxOUT8	G3
TxOUT/RxIN1	TxIN/RxOUT9	G4
	TxIN/RxOUT12	G5
	TxIN/RxOUT13	G6
	TxIN/RxOUT14	G7
	TxIN/RxOUT15	B2
	TxIN/RxOUT18	B3
	TxIN/RxOUT19	B4
TxOUT/RxIN2	TxIN/RxOUT20	B5
	TxIN/RxOUT21	B6
	TxIN/RxOUT22	B7
	TxIN/RxOUT24	HSYNC
	TxIN/RxOUT25	VSYNC
	TxIN/RxOUT26	DEN
	TxIN/RxOUT27	R0
TxOUT/RxIN3	TxIN/RxOUT5	R1
	TxIN/RxOUT10	G0
	TxIN/RxOUT11	G1
	TxIN/RxOUT16	B0
	TxIN/RxOUT17	B1
	TxIN/RxOUT23	RESERVED

### 5.3 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																										GRAY SCALE LEVEL			
		RED									GREEN									BLUE											
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
	DARK ↑	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2	
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R1020		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R1021		
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022		
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023	
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
		0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2	
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G1020		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G1021		
		0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G1022		
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G1023		
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2	
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B1020		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	1	0	1	1	1	1	1	1	B1021	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B1022	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B1023	

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

## 6. Interface Timing

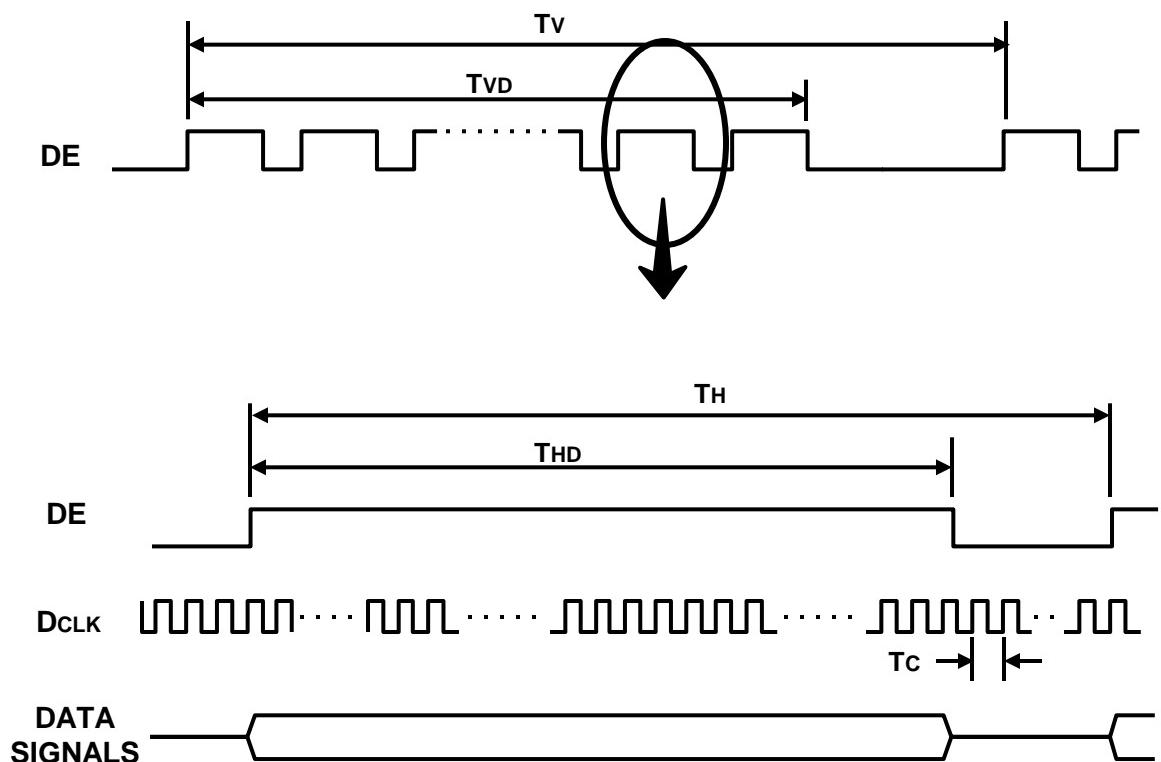
### 6.1 Timing Parameters (Timing-Controller)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	260	297	305	MHz	
Frame Frequency	Cycle	$T_V$	95	120	125	Hz	
		$T_H$	120	135	140	Khz	
Vertical Display Term	Active Display Period	$T_{VD}$	-	1080	-	Lines	
	Vertical Total	$T_{VB}$	1110	1185	1350	Lines	
Horizontal Display Term	Active Display Period	$T_{HD}$	-	1920	-	Clocks	
	Horizontal Total	$T_H$	2092	2120	2596	clocks	

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

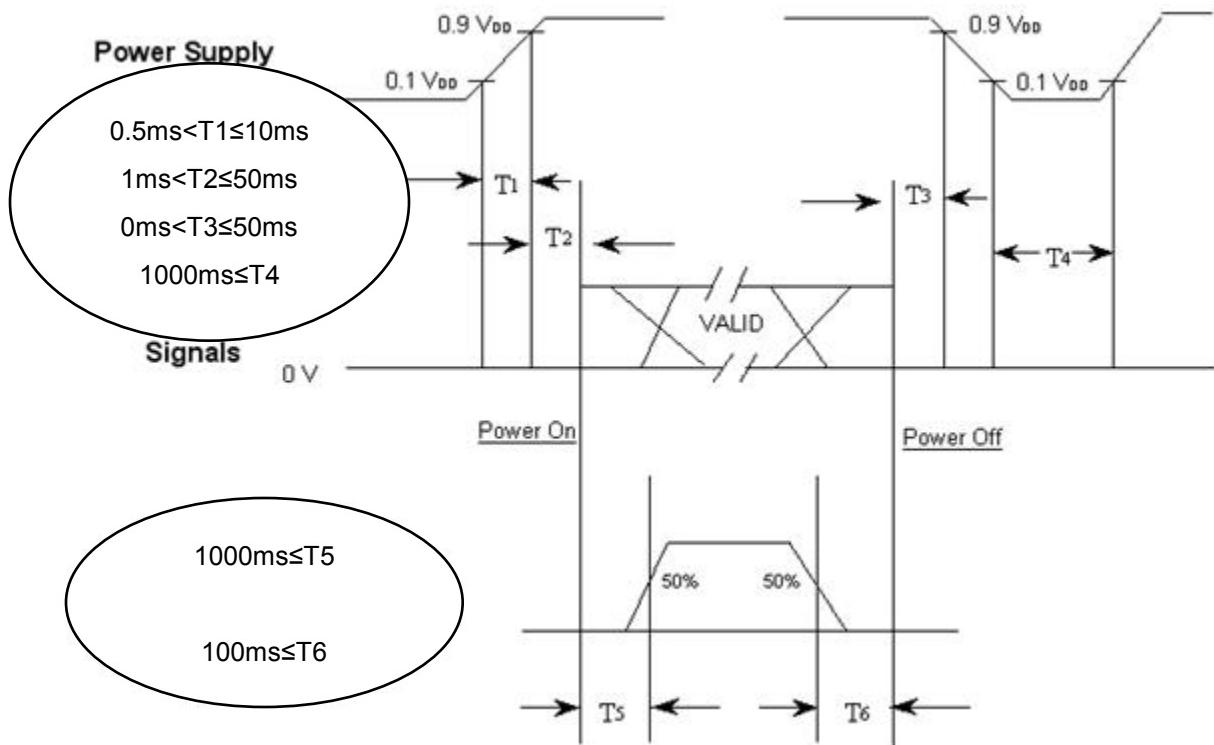
- (1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system
- (2) Internal  $V_{DD} = 3.3V$
- (3) Spread spectrum
  - Modulation rate (max) :  $\pm 1.5\%$
  - Modulation Frequency : 30 ~ 300KHz

## 6.2 Timing diagrams of interface signal ( DE only mode )



### 6.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 :  $V_{DD}$  rising time from 10% to 90%

T2 : The time from  $V_{DD}$  to valid data at power ON.

T3 : The time from valid data off to  $V_{DD}$  off at power Off.

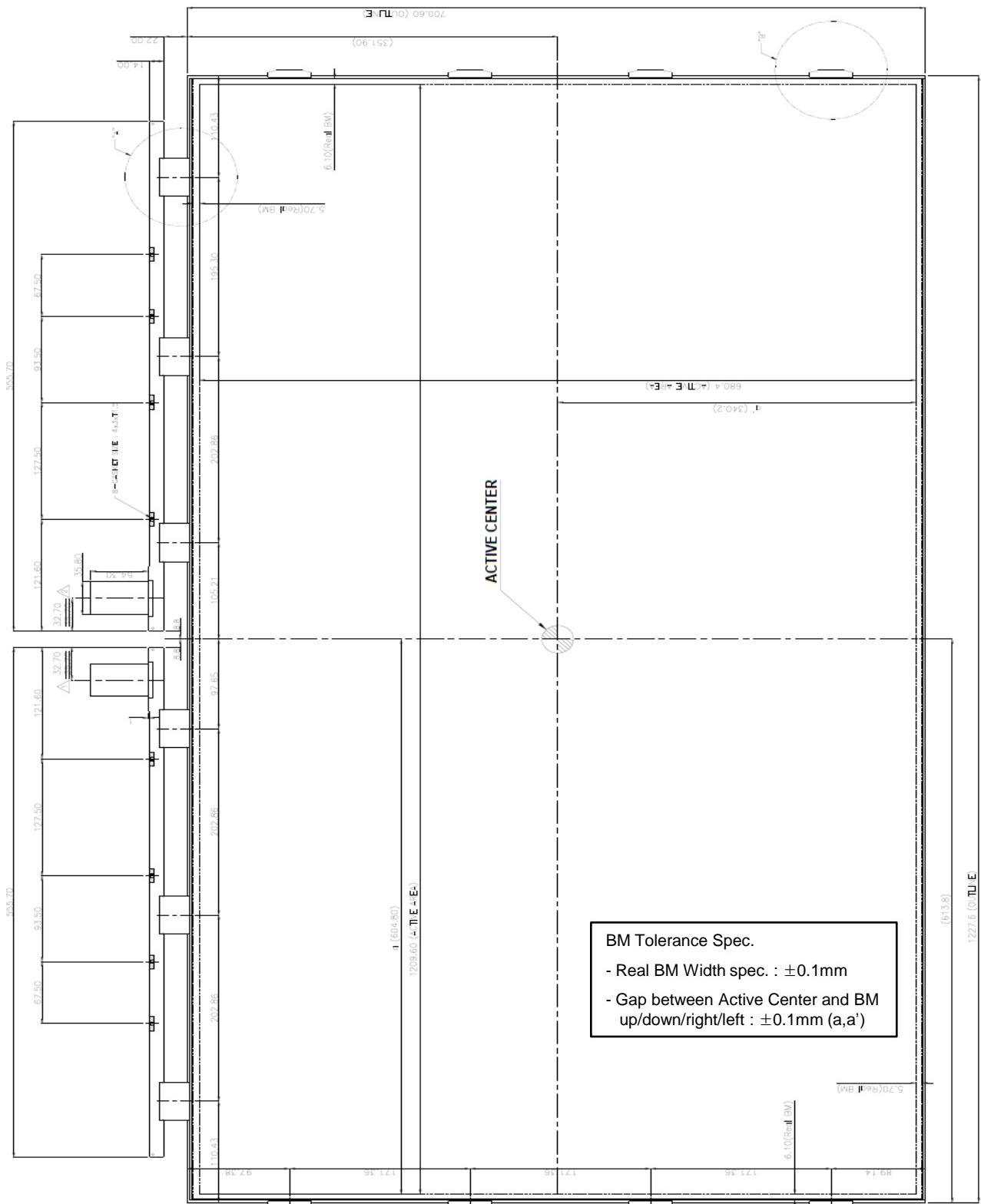
T4 :  $V_{DD}$  off time for Windows restart

T5 : The time from valid data to B/L enable at power ON.

T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of  $V_{DD}$ .
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of  $V_{DD} = \text{off level}$ , please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

## 7. Outline Dimension



## 8. Reliability Test

Item	Test condition	Quantity
HTOL	50°C / 500HR	4EA
HTOL2	60°C / 500HR	4EA
LTOL	-5°C / 500HR	4EA
THB	50°C / 90%RH / 500HR	4EA
Temperature Humidity Stress	-20°C~65°C, 0~90%RH, 2cycle / 194HR	4EA
Image Sticking	25°C / Mosaic pattern(9*10) 12hrs Rolling pattern 12hrs / 3cycles / 72HR	4EA
Altitude	-40°C~50°C, 0m(oft) ~ 13,700m(45,000ft) / 72.5HR	4EA
Open Cell Complex Test	WHTS / HTS / LTS / TRAY FALL / 600HR	10EA
ESD	Input : ±7kV / Output : ±4kV / 1Time, 1Point	4EA

### [ Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these should be no change which may affect practical display functions.

\* HTOL/ LTOL : High/Low Temperature Operating Life

\*\* THB : Temperature Humidity Bias

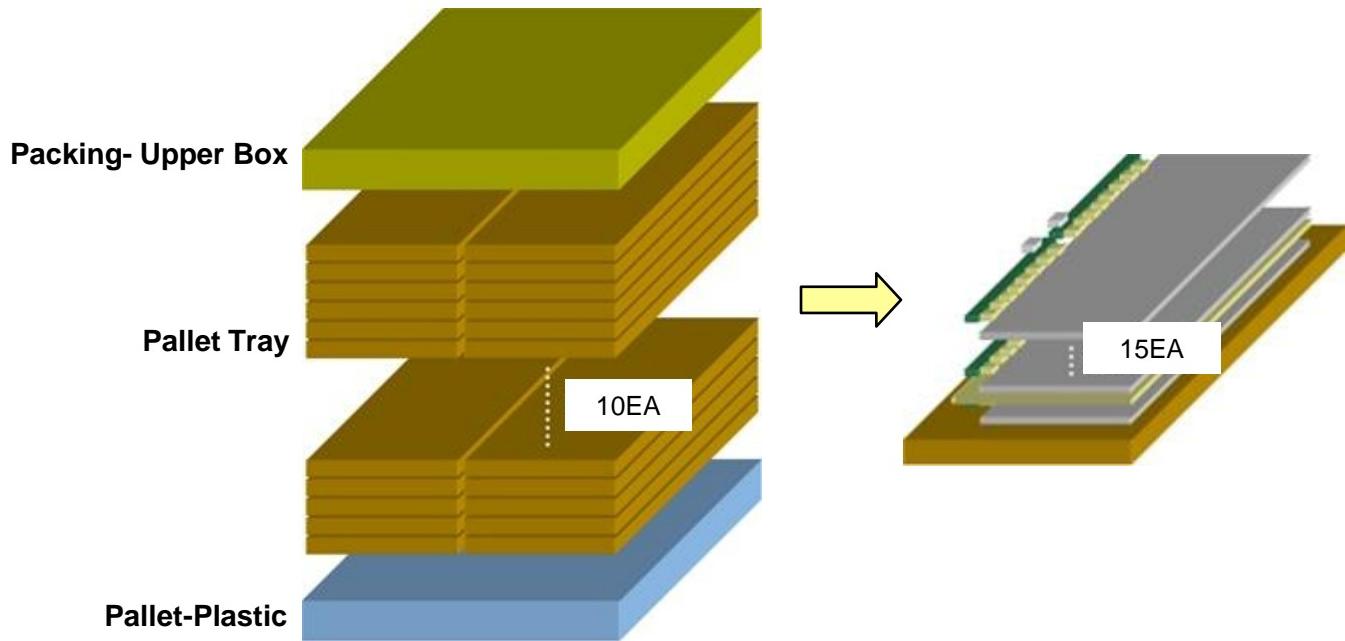
\*\*\* HTS/LTS : High/Low Temperature Storage

\*\*\*\* WHTS : Wet High Temperature Storage

## 9. PACKING

### 9.1 CARTON (Internal Package)

- (1) Packing Form Corrugated fiberboard box and corrugated cardboard as shock absorber
- (2) Packing Method



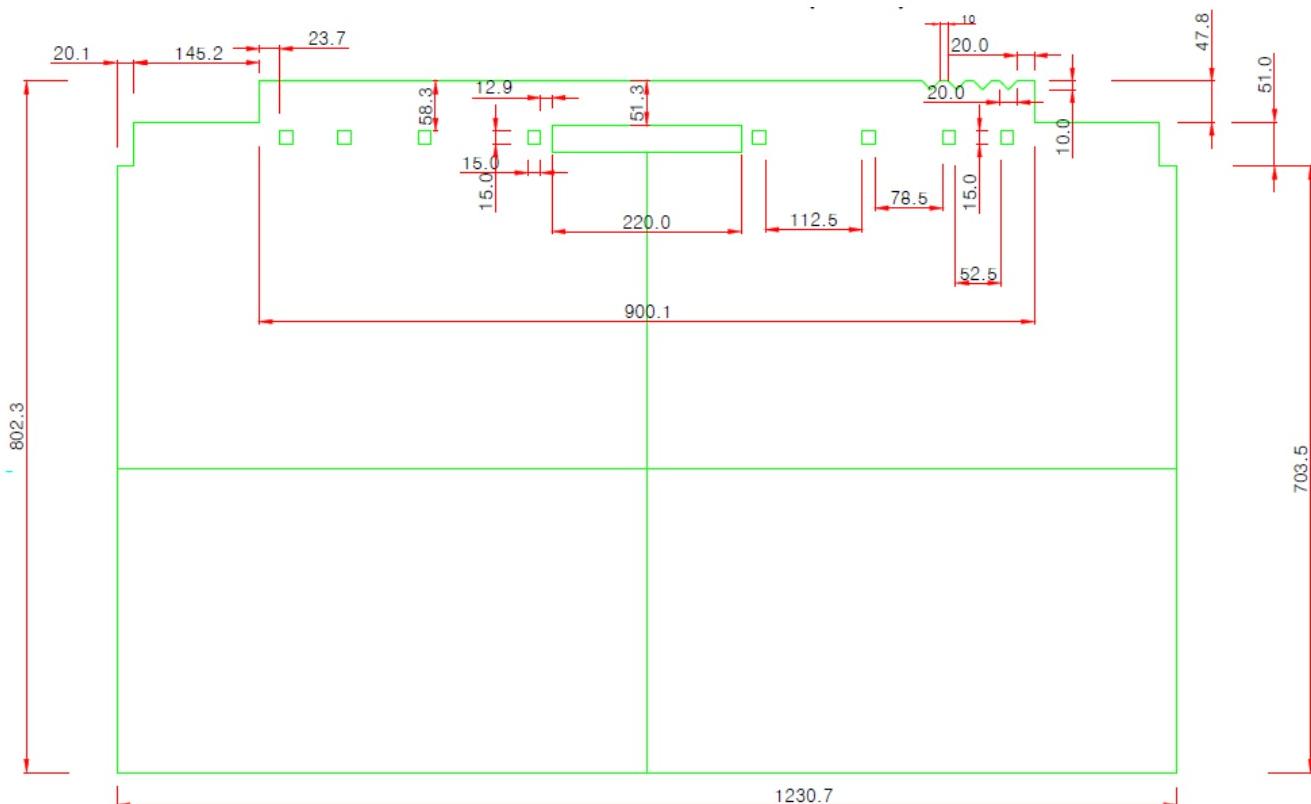
### 9.2 Packing Specification

ITEM	Specification	Remark
LCD Packing	150ea / Box	<ul style="list-style-type: none"><li>1. 3.35 Kg / LCD (15[ea]/Tray, 150[ea]/Pallet)</li><li>2. 0.04 Kg / Middle sheet (18[ea]/Tray, 180[ea]/Pallet)</li><li>3. 3.04 Kg / Panel tray (10ea/Pallet)</li><li>4. 1.82 Kg / Packing Box (1ea)</li></ul>
Pallet-Wood	1Box / Pallet	<ul style="list-style-type: none"><li>1. Pallet weight = 21 kg</li><li>2. 21 Kg / Pallet</li></ul>
Pallet size	H x V x height	1,360mm(H) x 940mm(V) x 1,205mm(height)
Pallet weight	566.42 kg	Pallet(21kg) + Panel tray(30.4kg) + Panel(502.5kg) + Middle sheet(7.2Kg) + Packing Box (5.32kg)

### 9.3 Middle Sheet Specification

ITEM	Specification	
Measurement Condition	Temperature	25 °C
	Humidity	20% ~ 60%
Material	Polyethylene	
Surface Resistance	$10^6 \sim 10^{9.9} \Omega$	
Frictional Voltage	Static (No Friction)	Less than 200V
	Dynamic (Friction : 5times at once)	Less than 500V

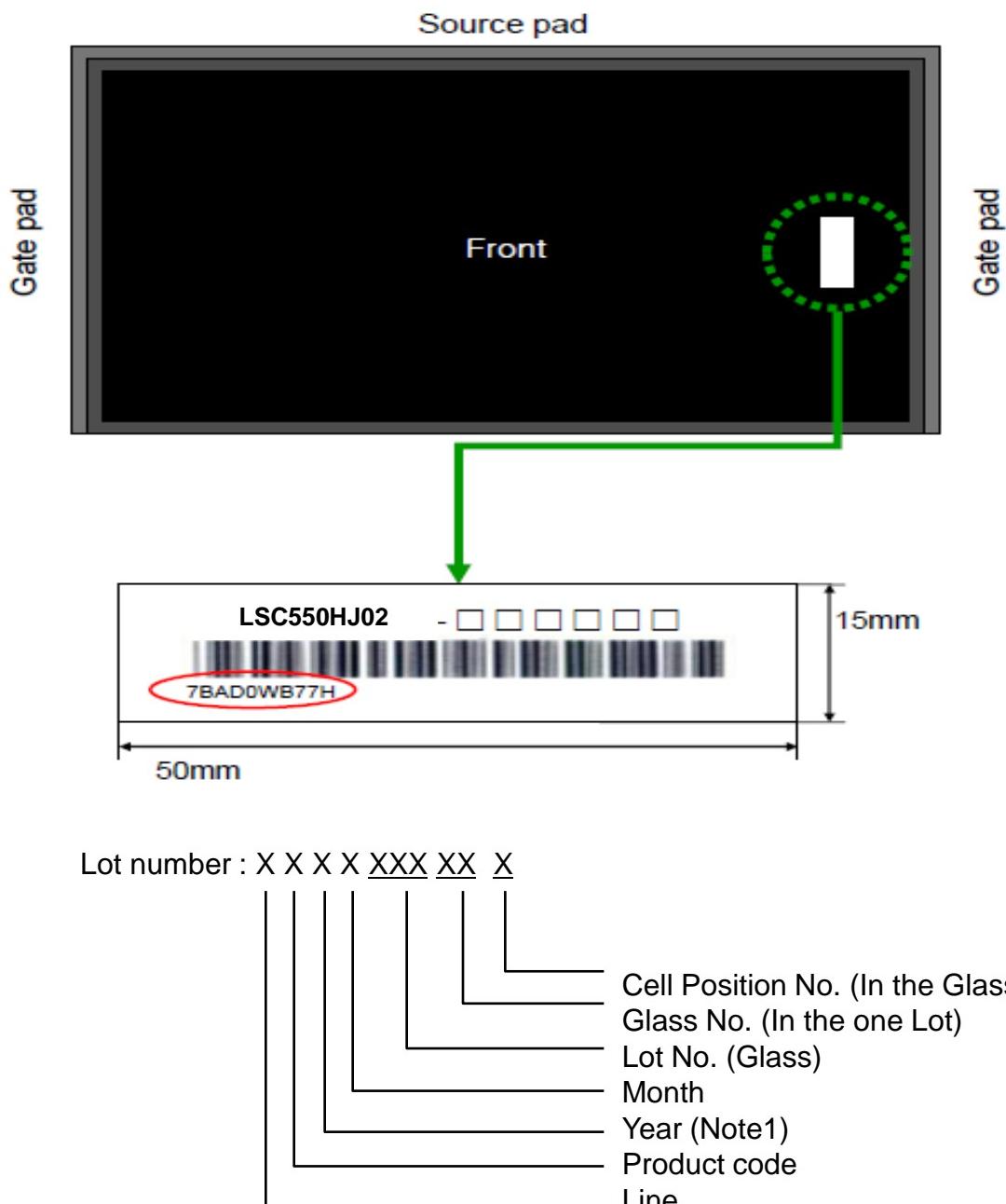
### 9.4 Middle Sheet Outline Dimension



## 10. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

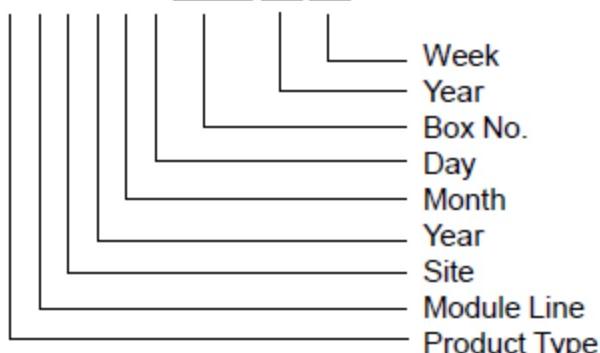
### (1) Cell Label



(2) Box Label



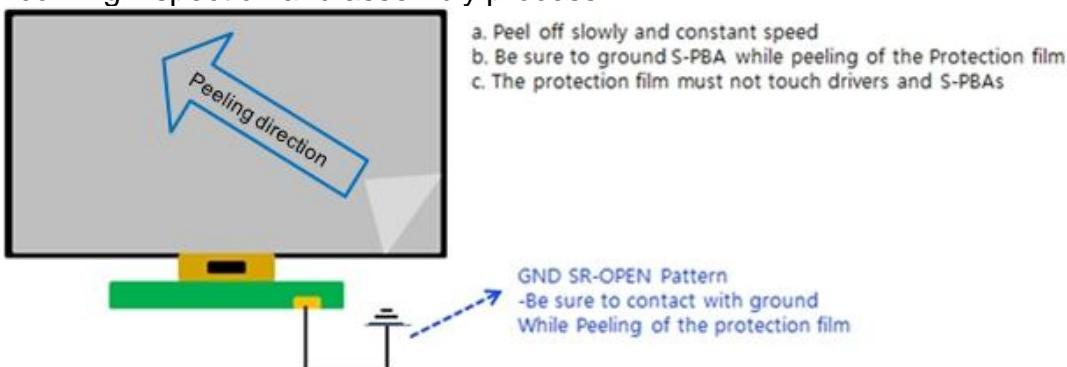
BOX Serial : X X X X X X XXXX XX XX



## 11. General Precautions

### 11.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFL back light.
- (c) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (f) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (h) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.
- (i) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.



- (k) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module
- (l) Pins of I/F connector should not be touched directly with bare hands.

\* Process Control Standard

	Item	Management standard value and performance standard
1	Anti-static mat (shelf)	1 to 50 [Mohm]
2	Anti-static mat (floor, desk)	1 to 100 [Mohm]
3	Ionizer	Attenuate from $\pm 1,000V$ to $\pm 100V$ within 2 sec
4	Anti-static wrist band	0.8 to 10 [Mohm]
5	Anti-static wrist band entry and ground resistance	Below 1,000 [ohm]
6	Temperature	0 ~ 35°C
7	Humidity	60 to 70 [%RH]

## 11.2 Storage

We highly recommend to comply with the criteria in the table below

ITEM	Unit	Min.		Max.							
Storage Temperature	(°C)	5		40							
Storage Humidity	(%rH)	35		75							
Storage life	6 months										
Storage Condition	<ul style="list-style-type: none"><li>- The storage room should provide good ventilation and temperature control.</li><li>- Products should not be placed on the floor, but on the Pallet away from a wall.</li><li>- Prevent products from direct sunlight, moisture nor water; Be cautious of a build up of condensation.</li><li>- Avoid other hazardous environment while storing goods.</li></ul>										
	After	1 month	2 month	3 month	4month	5 month					
	Baking	No backing	50°C 10% 24Hr	50°C 10% 48Hr							

## 11.3 Operation

- Do not connect or disconnect the Module in the "Power On" condition.
- Power supply should always be turned on/off by the "Power on/off sequence"
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

## 11.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions.  
Normal condition is defined as below;
  - Temperature :  $20 \pm 15^{\circ}\text{C}$
  - Humidity :  $55 \pm 20\%$
  - Display pattern : continually changing pattern (Not stationary)

- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc..., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

## 11.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. ( supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)  
Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.  
To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.